

주식회사 해치텍 2023 년 채용 모집 공고

모집학과	전기/전자/반도체 공학과
자격요건	자격요건 - 학력 : 학사 이상 - 경력 : 신입 및 경력
근무조건	- 근무형태 : 정규직 - 근무부서 : 설계팀 - 근무요일 : 월~금, 주52시간 선택근로제(자율 출근제도 적용) - 근무지역 : 서울시 강남구 선릉로 112길 - 연장수당 : 법정지급기준 적용(근로기준법 제 56 조 적용) - 급 여 : 회사 내규에 따름
복리후생	- 중식 별도 제공 - 퇴직연금(DC) 가입 - 종합검진(KMI검진센터, 차음검진센터 등) - 민간보험 가입(실비보험 또는 건강보험 선택(10년 만기)) - 휴게실, 다과 및 커피 제공

접수기간 : 상시 채용

작 성 일 : 2023 년 12 월 01 일

담 당 자 : 전사관리팀 송미화 차장(전화 043-715-9034)

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주식회사 해치텍



Analog Circuit Design & Development Engineer

Job description

As a design & development engineer, you will collaborate closely with product and system engineers in company's R&D center (Gangnam, Seoul) and have the opportunity to contribute to the evolution of company's product and technology.

In this position, you will develop analog circuits and follow up company's development process for on-time transfer to mass production.

Your responsibilities will include:

- Use a suite of software environments for the behavioral modeling and the verification of complex mixed-signal integrated circuit systems.
- Develop essential analog circuit blocks such as amplifiers, comparators, bandgap references, oscillators, and analog-to-digital converters.
- Validate product's datasheet specifications with top-level simulations.
- Evaluate and debug implemented circuits at either a wafer or a product level.

Qualifications

The successful candidate will possess either 2+ years field experience or a master's degree in electrical engineering (including coursework applicable to analog integrated circuit design, and strong theoretical background in circuit analysis, microelectronic device, or signal processing).

- Experience with schematic entry, layout software, integrated circuit modeling
- Experience with circuit simulation using SPICE-like simulators.
- Excellent communication, documentation, problem-solving and analytical skills.
- Understanding of laboratory procedures and basic electronic lab instruments such as oscilloscopes, voltmeters, and soldering tools.
- Familiarity with UNIX-based operating systems.
- Familiarity with any of the following software tools in beneficial:
Cadence Analog Design Environment, SPICE, Verilog.